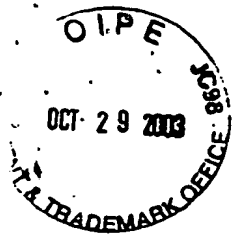




Application Data Sheet

APPLICATION INFORMATION

Application Number:: 09/871,978
Filing Date:: 06/04/01
Application Type:: Reissue
Subject Matter:: Utility
Suggested classification::
Suggested Group Art Unit::
CD-ROM or CD-R?: None
Number of CD Disks:
Number of Copies of CDs::
Sequence Submission?:
Computer Readable From (CRF)?:: No
Number of Copies of CRF::
Title:: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
COMPRISING RAM WITH COMMAND DECODE
SYSTEM AND LOGIC CIRCUIT INTEGRATED INTO
A SINGLE CHIP AND TESTING METHOD OF THE
RAM WITH COMMAND DECODE SYSTEM
Attorney Docket Number:: 401191
Request for Early Publication?: No
Request for Non-Publication?: No
Suggested Drawing Figure::
Total Drawing Sheets:: 15
Small Entity?: No
Latin Name::
Variety denomination name::
Petition Included?: No
Petition Type::
Licensed US Govt. Agency::
Contract or Grant Numbers::
Secrecy Order in Parent Appl.?: No



APPLICANT INFORMATION

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REPRESENTATIVE INFORMATION

Representative Customer Number:: 23548

Representative Designation:: Registration Number:: Representative Name::

DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This application is a	Reissue of	08/964.236	11/04/97

FOREIGN APPLICATION INFORMATION

Country::	Application Number::	Filing Date::	Priority Claimed
Japan	9-086600	04/04/97	Yes

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